

## **In the Specification**

Please amend paragraph 7 as follows:

**[0007]** Figure 1 is a functional block diagram of a system 10 according to one embodiment of the present invention.

Please amend paragraph 18 as follows:

**[0018]** Figure 1 is a functional block diagram of a system 10 according to one embodiment of the present invention. An optional modulated analog PLL 12 receives a reference signal, Fref. A frequency synthesizer 14 receives the output of the PLL, Fdither, as an input frequency and the reference signal Fref to generate the video clock frequency signal Fout. Configuration registers 16 transceive data and control with the PLL 12 and the frequency synthesizer 14.

Please amend paragraph 27 as follows:

**[0027]** SecondEdgeRemovalOffset accomplishes this by specifying the number of clocks to delay the second pulse removal. This is a configuration setting that comes from the microprocessor and is applied to a delay 35, which develops an output corresponding to the number of clocks by which the second removal pulse is delayed. An output of the delay 35 is applied through logic to a D-type Flip-flop that is clocked by the Fdither signal to generate the Fout signal. A divider 39 also receives the Fout signal and is clocked by the Fdither signal to generate a divided clock signal.

Please amend paragraph 35 as follows:

**[0035]** Since the predictor can get close, but is not able to track faster changes and may not estimate exactly correctly, ~~—, The the~~ corrector looks at each sample period and generates an error term. This error term is then normalized to the number of clocks per period (for example: remove 1 clock during the sample period, which usually has 8 clocks, so remove 1/8 of a clock each clock during the next sample period). This error term is added to the Predictor's so that the accumulator can generate the best position and number of clocks to remove as possible.

Please amend paragraph 36 as follows:

[0036] The embodiment disclosed in Figure 6 can handle removing many pulses during each sample period in a very nice way. This implementation provides more flexibility to choose frequencies since it is able to remove more than one pulse per sample period.